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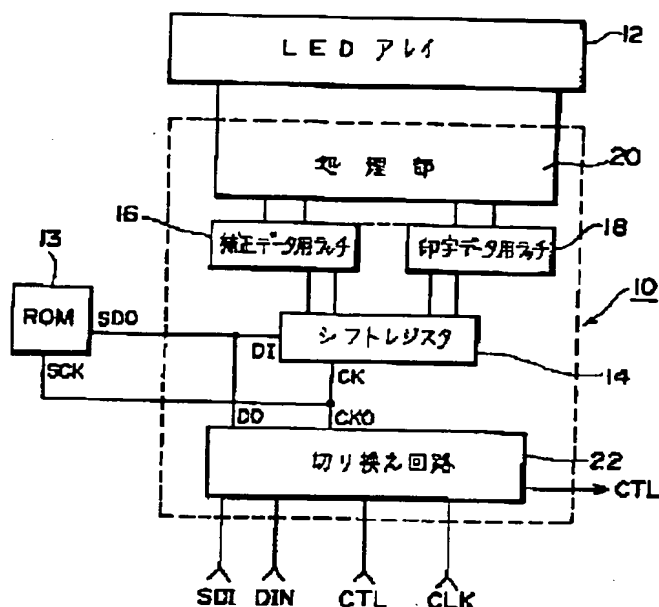
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TITLE : PRINTING HEAD DRIVING CIRCUIT
AND PRINTING HEAD



ABSTRACT : **PURPOSE:** To provide a simple and inexpensive printing head driving circuit capable of transmitting the correction data stored in an ROM requiring different clocks and the printing data supplied from the outside by using the clock input terminal and data input terminal of the same data shift register.

CONSTITUTION: The clock changeover means of a changeover circuit 22 changes over a clock for correction data and a clock for printing data to selectively supply them to the clock (CK) terminal of a shift register 14. A data changeover means changes over printing data or correction data corresponding to the supply of the clock for correction data and the clock for printing data to selectively supply the same to the data line(DI) terminal of the shift register 14. Therefore, it becomes possible to hold a data shift register having a clock input terminal and a data input terminal in common and the rationalization of planning and the miniaturization of a circuit can be achieved.

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